CLAIMS

WHAT IS CLAIMED IS:

1. A system, comprising:

a power supply configured to provide a standby signal, wherein the power supply is further configured to receive a power up signal; and

a delay circuit coupled to receive the standby signal, wherein the delay circuit is configured to provide the power up signal to the power supply after a predetermined delay in response to receiving the standby signal.

2. The system of claim 1, further comprising:

a stabilizer circuit coupled between the standby signal and the power up signal, wherein the stabilizer circuit is configured to provide a stable transition in the receipt of the power up signal.

3. The system of claim 2, further comprising:

an integrated circuit coupled to receive the standby signal from the power supply.

4. A system, comprising:

a detection circuit configured to receive a standby signal from a power supply, wherein the detection circuit is configured to deliver a control signal; and

a delay circuit coupled to receive the control signal, wherein the delay circuit is configured to deliver a delayed control signal for the power supply in response to the control signal after a predetermined period of time.

5. The system of claim 4, further comprising;

a stabilizer circuit configured to receive the standby signal and to receive the delayed control signal, wherein the stabilizer circuit is further configured to provide the delayed control signal to the power supply to ensure a stable transition during the receipt of the delayed control signal by the power supply.

6. The system of claim 4, further comprising:

the power supply coupled to provide the standby signal to the detection circuit, wherein the power supply is further coupled to receive the delayed control signal.

7. A system, comprising:

a delay circuit configured to receive a standby signal from a power supply, wherein the delay circuit is configured to deliver a delayed standby signal a predetermined period of time after receiving the standby signal ; and

a detection circuit configured to receive the delayed standby signal, wherein the detection circuit is configured to deliver a control signal for a power supply in response to receiving the delayed standby signal.

8. The system of claim 7, further comprising;

a stabilizer circuit configured to receive the standby signal and to receive the control signal, wherein the stabilizer circuit is further configured to provide the control signal to the power supply to ensure a stable transition during the receipt of the control signal by the power supply.

9. The system of claim 7, further comprising:

the power supply coupled to provide the standby signal to the delay circuit, wherein the power supply is further coupled to receive the control signal.

10. A method for providing power to a computer system, the method comprising:

providing a standby signal;

receiving a power up signal;

delaying the power up signal; and

passing the power up signal to the computer system after delaying the power up signal.

11. The method of claim 10, wherein providing a standby signal includes providing a 5VSB signal from a power supply.

12. The method of claim 10, wherein delaying the power up signal includes delaying the power up signal for a predetermined period of time before passing the power up signal to the computer system.

13. The method of claim 10, further comprising:

receiving the standby signal; and

outputting the power up signal in response to receiving the standby signal.

14. The method of claim 10, further comprising:

providing a stable transition from inactive to active for a power on signal at a power supply.

15. A system comprising:

means for receiving a power up signal;

means for delaying the power up signal; and

means for passing the power up signal after delaying the power up signal.

16. The system of claim 15, further comprising:

means for receiving the power up signal after delaying the power up signal; and

means for providing a stable transition from inactive to active for a power on signal at a power supply in response to receiving the power up signal after delaying the power up signal.

17. A system comprising:

an integrated circuit;

a power supply coupled to provide power to the integrated circuit, wherein the power supply is further configured to provide a standby signal to the integrated circuit, wherein the power is further configured to receive a power up signal;

a detection circuit coupled to receive the standby signal, wherein the detection circuit is configured to output a power on signal for the power supply in response to receiving the standby signal;

a delay circuit coupled to receive the power on signal for the power supply from the detection circuit, wherein the delay circuit is configured to output a delayed power on signal for the power supply in response to receiving the power on signal after a predetermined period of time; and

wherein the delay circuit is configured to provide the delayed power on signal to the power supply as the power up signal once the predetermined period of time has passed since the delay circuit received the power on signal.

18. The system of claim 17, further comprising:

a stabilizer circuit coupled between the delay circuit and the power supply, wherein the stabilizer circuit is configured to receive the delayed power on signal and to provide the delayed power on signal to the power supply for the delay circuit, wherein the

stabilizer circuit is further configured to provide a stable transition from inactive to active for the power up signal at a power supply.

19. The system of claim 18, wherein the stabilizer circuit is further configured to receive the standby signal and to provide the standby signal to the power supply as the power up signal to keep the power up signal inactive.